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Date February 2, 2004

Time

File no. Our Ref: XA-9596

Pages including cover 16

Re: Appln. No.: 10/041,601; GAU: 2818

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XA-9596

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Kiyoshi NAKAI et al.

Appln. No.: 10/041,601

Group Art Unit: 2818

Filed: January 10, 2002

Examiner: T. Nguyen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed September 3, 2003, please amend the above-identified patent application as indicated below.

Amendments to the specification begin on page 2 of this paper.

Amendments to the claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 8 of this paper.